# EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 5 | program adj counter with fpga | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:46 |
| L2 | 4157 | count adj field | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:47 |
| L3 | 110 | cache with 2 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:47 |
| L4 | 12821 | threshold near3 count$3 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:47 |
| L5 | 46006 | (call$3 or exception$1 or trap$4 or interrupt$3 or branch$3) near5 (count$3 or threshold$2) | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:48 |
| L6 | 3376 | (fpga or dsp) and 5 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:48 |
| L7 | 118 | (fpga or dsp) with 5 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:48 |
| L8 | 1837 | loop$3 near3 threshold | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:49 |
| L9 | 218 | 5 and 8 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:49 |
| L10 | 18 | (fpga or dsp) and 9 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:49 |
| L11 | 2626 | iteration$3 near3 count$3 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:49 |
| L13 | 605 | 5 and 11 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 17:50 |
| L14 | 218 | (fpga or dsp) and 13 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:25 |
| L15 | 7568 | (threshold or limit$3) near3 instruction$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:27 |
| L16 | 138 | 15 with (iteration$1 or loop$1) | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:27 |

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|---|---|---|---|---|---|---|
| L17 | 8 | branch$3 near3 fpga | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:27 |
| L18 | 203 | call$3 near3 fpga | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:29 |
| L19 | 38 | fpga near3 configur$5 near3 (count$3 or timer) | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:32 |
| L20 | 35 | count$3 near3 threshold with (fpga or dsp) | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:32 |
| L21 | 34 | count adj field near3 cache | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:32 |
| L22 | 554 | execution adj count$3 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:33 |
| L23 | 2 | 22 with exception$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:33 |
| L24 | 11 | reconfigur$4 adj instruction with (dsp or fpga) | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:33 |
| L25 | 11 | reconfigur$4 near1 instruction with (dsp or fpga) | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:34 |
| L26 | 2 | threshold near3 repetition$1 near3 instruction$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:34 |
| L27 | 2 | threshold near3 repetition$1 with instruction$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:34 |
| L28 | 34 | count near3 repetition$1 with instruction$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:36 |
| L29 | 46 | parallel near3 program adj counter | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:36 |
| L31 | 2452 | register$1 near3 program adj counter$1 | US-PGPUB; USPAT; EPO | OR | OFF | 2006/06/22 18:37 |
| L32 | 31278 | coprocessor or dsp | USPAT | OR | OFF | 2006/06/22 18:37 |
| L33 | 7 | 29 and 32 | USPAT | OR | OFF | 2006/06/22 18:38 |
| L34 | 275 | 712/226.ccls. | USPAT | OR | OFF | 2006/06/22 18:38 |
| L35 | 134 | cache and 34 | USPAT | OR | OFF | 2006/06/22 18:38 |

# EAST Search History

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| L36 | 0 | 33 and 35 | USPAT | OR | OFF | 2006/06/22 18:38 |
| L37 | 4 | simultaneous$2 near3 (read$2 or load near3 access$3) near3 (program adj counter) | USPAT | OR | OFF | 2006/06/22 18:40 |
| L38 | 0 | 35 and 37 | USPAT | OR | OFF | 2006/06/22 18:40 |
| L39 | 0 | concurrent$2 near3 (read$2 or load near3 access$3) near3 (program adj counter) | USPAT | OR | OFF | 2006/06/22 18:43 |
| L40 | 227 | 712/34.ccls. | USPAT | OR | OFF | 2006/06/22 18:44 |
| L41 | 0 | 29 and 40 | USPAT | OR | OFF | 2006/06/22 18:44 |